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		Application Number	10/673,539
		Filing Date	September 29, 2003
		First Named Inventor	Balocchi et al.
		Art Unit	2818
		Examiner Name	Dung Anh Le
Sheet 1	of 1	Attorney Docket Number	E3755-00002

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Du	A	WOLF, STANLEY, PhD. Multilevel-Interconnect Technology for VLSI and ULSI; Silicon Processing for the VSI ERA - Volume II: Process Integration; 1990; pp. 278-283; Lattice Press, Sunset Beach, CA.	

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